Microcontroller Framework for Radar Module Control

MIT Haystack Observatory

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Microcontroller Software Framework



Example Platform: GPS Coherence



Coherence Module



Coherence Module

- O Design by Frank Lind, James Marchese
- O Features
 - O PIC32MX695 microcontroller
 - O Analog Devices AD9548
- O Now under electrical test



Carrier Board Emulation

- O The carrier board microcontroller and Ethernet controller were simulated using the Ethernet Starter Kit
- O An evaluation board for the Maxim USB multiplexer was used to route commands to selected modules
- O A u-blox LEA-6T evaluation kit provides timing functionality
- O A Microchip USB Starter Kit II was used to simulate a coherence module



Carrier Board Firmware

- O The carrier board firmware makes use of the Microchip TCP/IP stack and USB Host stack, as well as components from the Microchip TCPIP Demonstration Application
- O The Ethernet Starter Kit is used to implement an HTTP server for web-based configuration of attached modules
- O Commands and new firmware may be sent to add-on modules
 - O Passed from web browser to carrier board via HTTP POST
 - O Passed from carrier board, through multiplexer, to modules via USB transfer
- O The carrier board firmware configures the GPS module for PPS and 10MHz timepulse operation

Microchip Ethernet Starter Kit

O PIC32MX795

O 512KB Program Memory

O 128KB RAM

- O USB debugging support
- O 10/100 Mbps Ethernet
- O PIC32 selected due to low cost
 - O Less than \$7 in large qty.



Module Emulation/Firmware

- O Microchip USB Starter Kit II used to simulate an add-on module
- O USB interface
- O Runs a modified Microchip bootloader
 - O Accepts USB-CDC commands
- O Remote firmware update through carrier board's web interface



u-blox LEA-6T GPS receiver

- O Two independently configurable timepulses—1Hz to 10MHz
- O Fixed mode operation
 - O Superior timing accuracy
- O ARM7 microcontroller
 - O USB communication
 - O On-the-fly reconfiguration



Maxim USB Multiplexer

- O USB hub initially used in carrier board design
 - O Not supported by Microchip USB stack
- O Analog 3:1 multiplexer
 - O One upstream host
 - O Up to three attached devices
 - O Only one device connected to host at a time
- O All devices powered simultaneously



Command Interface

(-)BI	http://gps	time/cmd.htm			- e	- Google	۹ 🖻 🗗
Microc	hip TCP/IP Stack	Demo App	+				
	MIT HAYST OBSER	ACK					
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Programming Interface

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Microchip TCP/IP	//gpstime/pgm.htm Stack Demo App	+				ioogle	2 合	
	STACK SERVATORY							
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Future Work

Further testing and validation
Firmware for coherence module PICs

SPI devices, AD9548 control

Host side improvements

USB to USART, flash data storage

Support for additional modules

Tuners (upconverters/downconverters)
Calibration sources
ADC/DAC Serial Control

References

- O Analog Devices Application Note 1002
- O Axelson, Jan. USB Complete, Third Edition
- O Esterline, John. Oscillator Phase Noise: Theory vs. Practicality.
- O Microchip Application Notes 833, 1247, 1388; microchip.com
- O u-blox GPS Compendium

Thanks

O My mentors, Jim Marchese and Bob Schaefer

- O Phil Erickson and Frank Lind
- O Ching Lue
- O Will Rogers
- O Atmospheric Science Group
- O KT Paul and Haystack Observatory
- O REU Class of 2011
- O NSF REU